

IEEE SOLID-STATE CIRCUITS SOCIETY ELECTION
For Election of Members to the Administrative Committee
For a Three-Year Term 1 January 2017 – 31 December 2019



JAN CRANINCKX (S'92-M'98-SM'07-F'14) obtained his Ms. and Ph.D. degree in Microelectronics summa cum laude from the ESAT-MICAS laboratories of the Katholieke Universiteit Leuven in 1992 and 1997, respectively. His Ph.D. work was on the design of low-phase noise CMOS integrated VCOs and PLLs for frequency synthesis.

From 1997 till 2002 he worked with Alcatel Microelectronics (later part of STMicroelectronics) as a senior RF engineer on the integration of RF transceivers for GSM, DECT, Bluetooth and WLAN.

In 2002 he joined IMEC (Leuven, Belgium) as principal scientist responsible for RF, analog and mixed signal circuit design, where he is currently Distinguished Member of Technical Staff. His research focuses on the design of RF transceiver front-ends in nanoscale CMOS, covering all aspects of RF, analog and data converter design.

Dr. Craninckx is an IEEE Fellow and has authored and co-authored more than 200 papers, book chapters, and patents. He is a member of the Technical Program Committee for ESSCIRC (2006-now) and ISSCC (2005-2011 & 2016-now), was the chair of the SSCS Benelux chapter (2006-2011), and SSCS Distinguished Lecturer (2012-2013). He was Associate Editor (2009-2016) and currently serves as Editor-in-Chief of the *JSSC*.

Statement: I have been an active member of the Solid-State Circuit Society, and I would like to transfer that enthusiasm to all members worldwide, to ensure that the SSCS is an important aspect of their professional life.

One key role for this is of course the publications, where circuit designers can read and contribute to knowledge sharing and advances in state of the art. As the new Editor-in-Chief of the *IEEE Journal of Solid-State Circuits*, I will ensure that our flagship publication remains the forefront place to be to know about all circuit design techniques and progress. But also conferences play a key role in that process, enabling the SSCS to reach out to its members.

Secondly, local chapter activity must continue to be stimulated. It is the first place where young engineers get to know the IEEE, but they must also be enabled to attract engineering professionals throughout their whole career.



TERRI S. FIEZ (S'82-M'85-S'87-M'91-SM'95-F'05) is Vice Chancellor for Research at University of Colorado. She was previously Head of Electrical Engineering and Computer Science at Oregon State University. During 2008 until mid 2009 she co-founded and served as CEO of a startup company Azuray Technologies. From 1990 to 1999 she was an assistant and associate professor at Washington State University. She has been very active professionally as a researcher and industry consultant in high performance analog and mixed-signal integrated circuits and innovative engineering education approaches. She has served in numerous leadership roles within IEEE including ISSCC and CICC conference committees, associate and guest editorships and distinguished lecturer for the Solid-State Circuits Society and the Circuits and Systems Society. Dr. Fiez was previously awarded the NSF Young Investigator Award, the IEEE Solid-State Circuit Pre-doctoral Fellowship, the 2006 IEEE Education Activities Board Innovative Education Award and the 2016 IEEE Undergraduate Teaching Award. She is a Fellow of the IEEE. She received her B.S. and M.S. in Electrical Engineering from University of Idaho and her Ph.D. in Electrical and Computer Engineering in 1990 from Oregon State University.



STEPHEN KOSONOCKY (M'90-SM'14) received his B.S., M.S. and Ph.D. degrees from Rutgers University in 1986, 1991 and 1994 respectively. In 1986 he joined Siemens Corporate Research Center, focusing on CMOS digital and analog design. In 1992 he joined Samsung working on video circuits, then in 1993 he joined Rutgers University focusing on keyword classification as part of his doctoral thesis. In 1994 he joined IBM T.J. Watson Research Center in Yorktown Heights, NY, working on eDRAM/SRAM, low-power microprocessor design and research management. In 2007 he joined Advanced Micro Devices in Fort Collins, CO where he is currently a Senior Fellow leading a low-power Advanced Development team. He has been the Chair of the Symposium on VLSI Circuits, Treasurer for Symposia on VLSI, Sub-Committee Chair for ISSCC, ISLPED TPC member, AMD representative to SRC Executive Technical Advisory Board, and the SSCS Membership Chair from 1998-2000. He has authored/co-authored 65 publications and 58 Patents.



TADAHIRO KURODA (M'88-SM'00-F'06) received the Ph.D. degree in Electrical Engineering from the University of Tokyo. In 1982, he joined Toshiba Corporation. In 2000, he moved to Keio University, where he has been a Professor since 2002. He was a Visiting MacKay Professor at the University of California, Berkeley in 2007. He has published more than 200 papers, including 37 ISSCC papers, 26 VLSI Symposium papers, 19 CICC papers and 16 A-SSCC papers. He wrote 27 books/chapters and filed 200 patents.

Prof. Kuroda is an IEEE Fellow. He is the Executive Committee chair of the Symposium on VLSI Technology and Circuits, and the Steering Committee chair of the A-SSCC. He served as the vice chair for ASP-DAC, sub-committee chairs for A-SSCC, ICCAD, SSDM and VLSI-DAT, and TPC members for the ISSCC, the Symposium on VLSI Circuits, CICC, DAC, ASP-DAC, ISLPED, SSDM, ISQED, and other international conferences. He was a member of AdCom (2008-2010), a Distinguished Lecturer, and a representative of Region 10 for the SSCS.



MAURITS ORTMANNS (S'04-M'04-SM'11) received the Dr.-Ing. from the University of Freiburg, Germany in 2004. From 2004-2005 he was with sciworx GmbH, Hannover, Germany as a project leader in mixed-signal electronics. From 2006-2007 he was Assistant Professor for Integrated Interface Circuits at the University of Freiburg, and since 2008 he is Full Professor and director of the Institute of Microelectronics at the University of Ulm, Germany. His research interests include mixed-signal integrated circuit design, self-correcting and reconfigurable analog circuits, with special emphasis on data converters and implantable electronics. He served as program committee member of ESSCirc, DATE, ECCTD, ICECS, as Associate Editor of *IEEE TCAS I* and *IEEE TCAS II*, as Guest Editor for *IEEE JSSC*, and as program and executive

committee member and European Regional Chair of ISSCC from 2012-2016. He holds several patents, is author of the book "Continuous-Time Sigma-Delta A/D Conversion", and published >200 IEEE journal and conference papers.



SENG-PAN U (BEN) (S'95-AM'00-M'01-SM'05-F'16) received the joint Ph.D. degree from University of Macau (UM) and Instituto Superior Técnico, Portugal in 2002. He is one of the pioneers and leaders with transformational impact and contributions on the development of the solid-state IC design research activities in both academe and industry in Macau. With past 20+ year effort, Macau has been placed on the map as one of the world-renowned centers in IC design. Currently, he is UM Professor and Deputy Director of State-Key Laboratory of Analog & Mixed-Signal VLSI, and also the co-founder & Site General Manager of Synopsys Macau Ltd (Former Chipidea Microelectronics Macau). He co-authored 140+ publications, 4

books and co-held 10 US patents. He received, as Macau founding chair, the 2012 SSCS Outstanding Chapter Award, 2014 ESSCIRC Best paper award, and he is also the advisor for student awards of SSCS Pre-doc Achievement Award, ISSCC Silk-Road Award, A-SSCC Student Design Contest and etc. He was IEEE SSCS Distinguished Lecturer and A-SSCC Tutorial Speaker. He is currently IEEE Fellow, and was elected as the "Scientific Chinese of the Year 2012" and recently appointed a member of the S&T Commission of China Ministry of Education. He is currently TPC of ISSCC, A-SSCC, RFIT, Analog sub-committee chair of VLSI-DAT and Editorial Board member of *Journal AICSP*.



INGRID VERBAUWHEDE (M'92-SM'00-F'13) is a Professor in the research group COSIC of the Electrical Engineering Department of the KU Leuven in Belgium. At COSIC, she leads the embedded systems and hardware group. She is also adjunct Professor at the EE department at UCLA, Los Angeles, CA. She joined COSIC in 2003 and UCLA in 1998. Before joining UCLA she worked at UC Berkeley as a post-doctoral researcher and visiting lecturer, and later at TCSI and Atmel Lab in Berkeley, CA.

She is a Member of IACR and a Fellow of IEEE. She was elected as member of the Royal Flemish Academy of Belgium for Science and the Arts in 2011. She is a recipient of an ERC Advanced Grant in 2016. She is a pioneer in the field of efficient and secure implementations of cryptographic algorithms in embedded context on ASIC, FPGA and embedded SW. She is the author and co-author of more than 300 publications at conferences, journals, book chapters and books. She graduated 27 Ph.D. students between 2004 and 2015, which have positions in academia and in industry, all over the world. She has been the general chair in 2012 and the program chair in 2007 of the IACR CHES (Cryptographic Hardware and Embedded Systems) workshop, which is the flagship venue for secure hardware design. She has been member of the program committee of a large number of conferences, including DAC, DATE, ISSCC, Usenix, SIPS, ISCAS, ISLPED, and more.



ALICE WANG (S'96-M'97-S'99-M'04-SM'09) received her Bachelors, Masters, and Ph.D. degrees in Electrical Engineering and Computer Science from the Massachusetts Institute of Technology, in 1997, 1998, and 2004, respectively. She wrote the paper "A 180-mV Subthreshold FFT Processor Using a Minimum Energy Design Methodology" with Professor Anantha Chandrakasan which inspired a new research field in ultra-low power technology. After her Ph.D., she spent 8 years at Texas Instruments developing low-power circuit and system technology for mobile, application processors and radios. Her work on low-power technology has been showcased in 30+ IEEE publications and she has co-authored two books. The last 5 years, as a Senior Director at MediaTek she has been working on high-performance, low-power processors. Her team was involved in the first quad-core, octa-core and deca-core for best power efficiency in Smartphones, Tablets and Smart TV's. She is also serving on the Technology Directions committee for the International Solid-State Circuits Conference. She is a long time supporter of Near-Vt and Sub-Vt research in university and industry.